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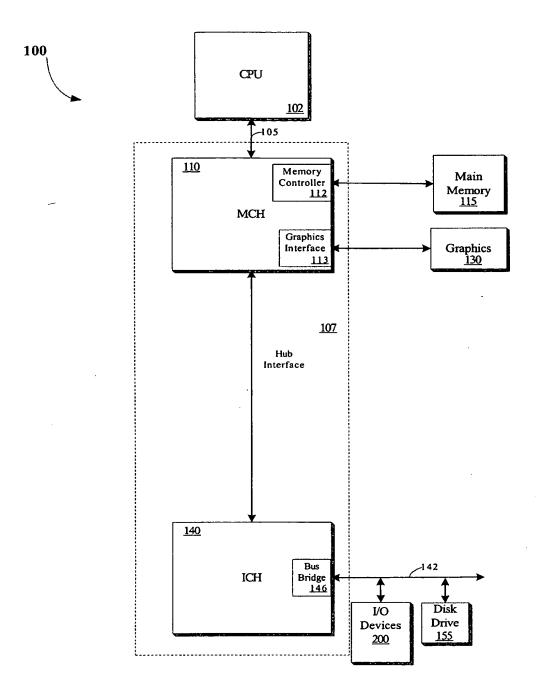


Fig. 1

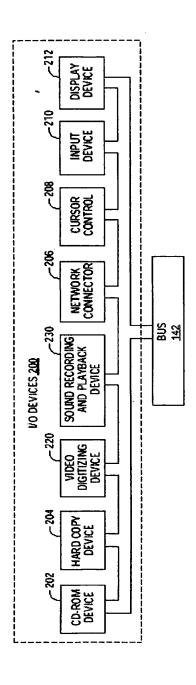


Fig. 2

Fig. 3a

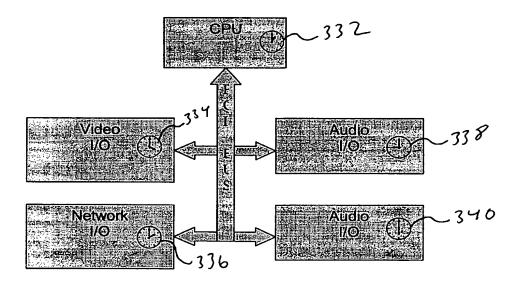


Fig. 3b

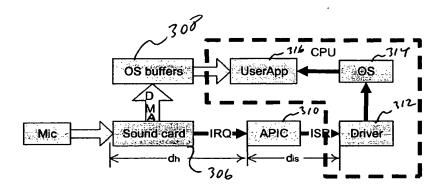
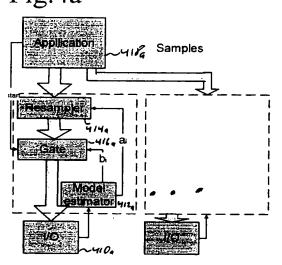


Fig.4a



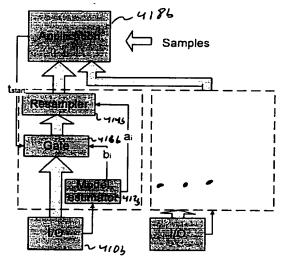


Fig. 4b

Issue Start a- L specifyItime in CPU clock units 502 I/O Levice Capturel Samples 504 Estimator collects Time stamp pairs 50 E offset Generate and re-sampling CoefficienTT 508 Gate opened samples when offset Valve reached 510 Resample chances sampline prequency according to re-sampling Coefficient 512

Fi6. 5